

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Shunpei Yamazaki et al.	Art Unit :	2871
Serial No. :	09/809,646	Examiner :	Thoi V. Duong
Filed :	March 16, 2001	Confirmation No.:	5011
Notice of Allowance Date: November 28, 2006			
Title :	SEMICONDUCTOR DISPLAY DEVICE AND MANUFACTURING METHOD THEREOF		

MAIL STOP 88201

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed November 28, 2006 enclosed is a completed issue fee transmittal form PTOL-85b. The fees in the amount of \$1700 for the issue fee (\$1400) and the publication fee (\$300) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: February 27, 2007


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